



Agilent 10529A Logic Comparator

Data Sheet

Inputs

Input Threshold: 1.4V nominal (1.8V nominal with socket board), TTL or DTL compatible.

Test IC Loading: Outputs driving Test IC inputs are loaded by 5 low-power TTL loads plus input of Reference IC. Test IC outputs are loaded by 2 low-power TTL loads.

Input Protection: Voltages $< -1\text{V}$ or $> 7\text{V}$ must be current limited to 10 mA.

Supply Voltage: $5\text{V} \pm 5\%$

Supply Protection: Supply voltage must be limited to 7V.

Maximum Current Consumption: 300 mA

Sensitivity:

Error Sensitivity: 200 ns with reference board or 300 ns with socket board. Errors greater than this are detected and stretched to at least 0.1 s.

Delay Variation Immunity: 50 ns. Errors shorter than this value are considered spurious and ignored.

Frequency Range: Maximum operational frequency varies with duty cycle. An error existing for a full clock cycle will be detected if the cycle rate is less than 3 MHz.

Temperature: 0°C to 55°C